MQ9208ET540

Isolated 18~36V input, Eighth-brick, single output 80W DC-DC Converter



FEATURES

- Compliant to RoHS II EU "Directive 2011/65/EU (-Z ⊳ versions)
 - Industry standard, DOSA compliant footprint 58.4mm x 22.8mm x 9.0 mm
 - 2.30 in x 0.90 in x 0.35 in
- Wide operating voltage:18~36V
- Tightly regulated single output: \triangleright 54V/80W
- Output power up to 80W
- High Efficiency 91% (54V output, Full load) ≻
- ⊳ Output Voltage adjust: 90% to 110% of VO,NOM
- Constant switching frequency
- Remote sense
- AAA Output over-current / short-circuit protection
- ⊳ Over-temperature protection
- ۶ Input under-voltage Protection
 - ON/OFF control polarity selectable
 - Negative: R/C connect Vin- for normal operation
 - 1 Positive: R/C floating or High Level for normal operation
- Monotonic start-up
- Wide Operating Temperature: -40°C~+85°C

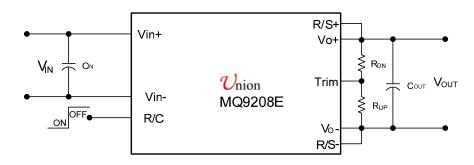
APPLICATIONS

- Industrial Equipment \triangleright

- \triangleright

Description

The MQ9208E Series Power Modules are isolated single output dc-dc converter that operates over a wide input voltage range of18V_{dc} to36V_{dc} and provide a precisely (1%) regulated dc output in standard 1/8-brick size. The module provides 54V_{dc} nominal output voltage rated for 80W output, achieves 91% high efficiency at 24Vdc input voltage application. Its 54Vdc voltage and high efficiency is ideal for POE applications which equips with 24V battery as backup power source. Standard features include remote On/Off, remote sense, output voltage adjustment, overvoltage, over current and over temperature protection.



- \triangleright Surveillance
- \triangleright Wireless Networks
- **Distribute Power Architecture**

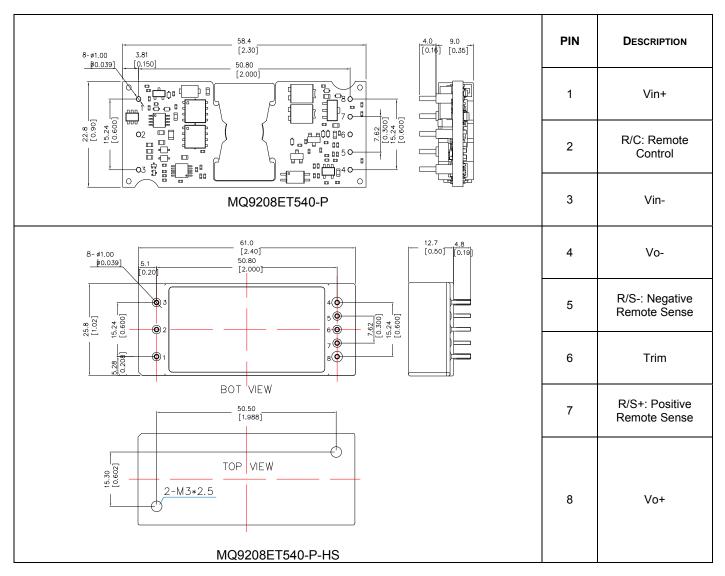
Performance Specifications(at TA=+25°C)

Madal	Input V _{IN} Range		Output			Efficiency
Model	(V)	Pour(W) V ₀ (V) Regulation			(%)	
				Line (%)	Load (%)	
MQ9208ET540	18~36	80	54	1	1	91

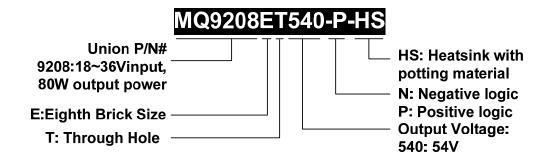
Mechanical Outline Diagram

Unit: millimeters and [inches]

Tolerances: x.x ±0.5mm [x.xx in. ±0.02in.], x.xx ±0.25mm [x.xxx in. ±0.010 in.], unless otherwise noted



Ordering Information





MQ9208ET540

Absolute Maximum Ratings

Note: These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance Specifications Table is not implied.

Parameter	Symbol	Min	Max	Unit
Input Voltage	VIN	18	36	V
Operating Ambient Temperature	TA	-40	85	°C
Storage Temperature	Tstg	-55	125	°C
Altitude			4000	m
I/O Isolation voltage (100% factory Hi-pot tested)			2250	V _{dc}

Electrical Specifications: (TA=+25°C)

Unless otherwise indicated, specifications apply overall operating input voltage, resistive load, and temperature conditions.

Operating Input VoltageVN182436V $_{ec}$ Mearem input CurrentIN_mmaxInt StransienttbdAdcInput Reflected Ripple Current, peak-to-peakIPtubdtbd A^2s Input Reflected Ripple Current, peak-to-peakIPtubdtbdmAopInput Reflected Ripple Current peak-to-peakIPtubdtbdmAopInput Reflected Ripple CurrenttbdmAdcmAdcmAdcInput Stand-by CurrenttbdubdmAdcmAdcInput Stand-by CurrenttbdubdmAdcmAdcInput Ripple Rejection (120H2)ubd50dBdBOutput Voltage Set-pointVo. set54VacVacOutput Voltage Set-pointVo. set53.5-54.5VacOutput Voltage Set-point Total Tolarace-0.20.55%0vo.setVacOutput Voltage and Noise on nominal output-0.40.75%vo.setVacUnter (Vin=Vin, min to Vin, MAX)0.40.75%vo.setLoad (0=fo, min to lo, max)1-100200mVmsRMS (6Hz to 20MHz bandwidth)-100200mVmsPeak-to-Peak (6Hz to 20MHz bandwidth)-100200mVmsPeak-to-Peak (6Hz to 20MHz bandwidth)-100200mVmsPeak-to-Peak (6Hz to 20MHz bandwidth)-10001.48AccOutput Current Untit InceptionIo, se-	Parameter	Symbol	Min	Тур	Max	Unit
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Input Reflected Ripple Current, peak-to-peak (GHz to 20MHz, 12µH source impedance; V _N =0V to 36V, lo= lowax; see Fig 1)ImageItalItalItalImAgeInput No Load Current (V _N = 24V, (lo = 0A, module enabled)Input Stand-by Current (V _N = 24V, module disabled)ItalItalImAdcInput Stand-by Current (V _N = 24V, module disabled)50ItalIdImAdcInput Stand-by Current (V _N = 24V, module disabled)Vo. set50IdIdInput Ripple Rejection (120Hz)Vo. set54VacVacOutput Voltage Set-Point Total Tolearone (Overall operating input voltage, resistive load, and temperature conditions until end of life)Vo. 53.5-54.5VacOutput Voltage Set-Point Total Tolearone (Overall operating input voltage, resistive load, and temperature conditions until end of life)-0.20.5%Vo.setOutput Ripple and Noise on nominal output (V _{IN=VIN,NOM} and lo=lown to lowax) Temperature (Toxis = -40°C to +100°C)-100200mVmmsRMS (SHz to 20MHz bandwidth)-100200mVmms-300600mVpscstRMS (SHz to 20MHz bandwidth)-10001.48AccAccOutput Ripple and K (SHz to 20MHz bandwidth)-10001.48AccOutput CurrentInto Linex, Vo=VO.SETIno, imItalAccOutput Ripple and Noise on nominal output (V _{IN=VIN,NOM, IC=25°C)Ino, imItalAccOutput CurrentIno Linex, Vo=VO.SET-40<}						
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$ \begin{array}{c c} (\Delta I_o/\Delta t=0.1 A/\mu s; V_{IN}=V_{IN,NOM}; Tc=25^{\circ}C; \\ \hline Tested with a 220 \mu F aluminum and a 10 \mu F ceramic capacitor across the load.) \\ Load Change from I_o= 25\%-50\%-25\% of I_{O,MAX}: \\ \hline Peak Deviation \\ Load Change from I_o= 50\%-75\%-50\% of I_{O,MAX}: \\ \hline Peak Deviation \\ \hline Load Change from I_o= 50\%-75\%-50\% of I_{O,MAX}: \\ \hline Peak Deviation \\ \hline Settling Time (V_o<10\% peak deviation) \\ \hline Load Change from I_o= 50\%-75\%-50\% of I_{O,MAX}: \\ \hline Peak Deviation \\ \hline Settling Time (V_o<10\% peak deviation) \\ \hline Load Change from I_o= 50\%-75\%-50\% of I_{O,MAX}: \\ \hline Peak Deviation \\ \hline Settling Time (V_o<10\% peak deviation) \\ \hline Load Change from I_o= 50\%-75\%-50\% of I_{O,MAX}: \\ \hline How the test of the test of the test of the test of test of$	Switching Frequency	f _{sw}	_	tbd	—	KHz
$ \begin{array}{c c} (\Delta I_o/\Delta t=0.1 A/\mu s; V_{IN}=V_{IN,NOM}; Tc=25^{\circ}C; \\ \hline Tested with a 220 \mu F aluminum and a 10 \mu F ceramic capacitor across the load.) \\ Load Change from I_o= 25\%-50\%-25\% of I_{O,MAX}: \\ \hline Peak Deviation \\ Load Change from I_o= 50\%-75\%-50\% of I_{O,MAX}: \\ \hline Peak Deviation \\ Feak Deviation \\ \hline Settling Time (V_o<10\% peak deviation) \\ \hline Load Change from I_o= 50\%-75\%-50\% of I_{O,MAX}: \\ \hline Peak Deviation \\ \hline Settling Time (V_o<10\% peak deviation) \\ \hline Settling Time (V_o<10\% peak deviati$	Dynamic Load Response					
Tested with a 220µF aluminum and a 10µF ceramic capacitor across the load.) Load Change from Io= 25%-50%-25% of Io,MAX: Peak Deviation Settling Time (Vo<10% peak deviation) Load Change from Io= 50%-75%-50% of Io,MAX: Peak Deviation V_{pk} 2 $\%V_{o,set}$ mSPeak Deviation Settling Time (Vo<10% peak deviation) Load Change from Io= 50%-75%-50% of Io,MAX: V_{pk} 2 $\%V_{o,set}$ Settling Time (Vo<10% peak deviation) Settling Time (Vo<10% peak deviation)						
Load Change from Io= 25%-50%-25% of Io,MAX: Peak Deviation Settling Time (Vo<10% peak deviation) Load Change from Io= 50%-75%-50% of Io,MAX: Peak Deviation V_{pk} 2 $\%V_{o,set}$ Note that the set of						
Peak Deviation V_{pk} 2 $%V_{o,set}$ Settling Time (Vo<10% peak deviation)						
Settling Time (Vo<10% peak deviation) Load Change from Io= 50%-75%-50% of Io,MAX: v_{pk} $ 2$ $ \sqrt{v_{0,set}}$ Peak Deviation Settling Time (Vo<10% peak deviation)						0/1/
Load Change from I_0 = 50%-75%-50% of $I_{0,MAX}$:IsIsIsPeak DeviationVpk-2-Settling Time (Vec10% peak deviation)Vpk-2-			-		-	- /
Sottling Time (Vector) $V_{pk} = 2 - %V_{o,set}$		٤		1.5	-	1115
		V.		2		0/1/
	Settling Time (Vo<10% peak deviation)	Vpk ts		∠ 1.5		%v₀,set mS



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		MQ9208ET54				
(ΔI _o /Δt=10%I _{O,max} /10μs; V _{IN} =V _{IN,NOM} ; Tc=25°C;]
Tested with a 470µF aluminum and a 10µF ceram	ic capacitor across the load,					
see Fig 7.						
Load Change from Io= 0%-120% of Io,MAX:	V _{pk}		2		%V _{o.set}	
Peak Deviation	vpĸ ts		1.5		mS	
Settling Time (Vo<10% peak deviation)	ls		1.5		1115	
Load Change from I ₀ = 120% to 50% of I _{O,MAX} :	Vpk		2		%V _{o.set}	
Peak Deviation			1.5		mS	
Settling Time ($V_0 < 10\%$ peak deviation)	ts	_	1.5		1115	

Note: use a minimum 220uF output capacitor. Recommended capacitor is Nichicon CD series, 220uF/35V. If the ambient

temperature is less than -20°C, use more than 3 of recommended minimum capacitors.

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This power module can be used in a wide variety of applications, ranging from simple standalone operation to being an integrated part of complex power architecture. To preserve maximum flexibility, internal fusing is not included. Always use an input line fuse, to achieve maximum safety and system protection. The safety agencies require a time-delay or fast-acting fuse with a maximum rating of 10A in the ungrounded input connection (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data sheet for further information.

Isolation Specifications

Parameter	Symbol	Min	Тур	Max	Unit
Isolation Capacitance	Ciso		15	_	nF
Isolation Resistance	Riso	10	_	—	MΩ

General Specifications

Parameter	Symbol	Min	Тур	Max	Unit
Calculated Reliability based upon Telcordia SR-332 Issue 2: Method I Case 3 (I_0 =80% $I_{O,MAX}$, T_A =40°C, airflow = 1m/S, 90% confidence)	FIT MTBF		TBD TBD		10 ⁹ /H _{ours} H _{ours}
Weight			TBD		g

Feature Specifications

Unless otherwise indicated, specifications apply overall operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

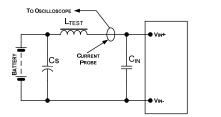
Parameter	Symbol	Min	Тур	Max	Unit
Remote On/Off Signal Interface (V _{IN} = V _{IN,MIN} to V _{IN,MAX} ,open collector or equivalent, Signal referenced to v _{IN} terminal) Negative Logic: device code suffix "N" Logic Low = module On, Logic High = module Off Positive Logic: device code suffix "P" Logic Low = module Off, Logic High = module On Logic Low - Remote On/Off Current Logic Low - On/Off Voltage Logic High Voltage – (Typ = Open Collector) Logic High maximum allowable leakage current	Ion/off Von/off Von/off Ion/off	 0 2.5 		1.0 1.2 10 50	mA Vdc Vdc μΑ
Turn-On Delay and Rise Times					
$(V_{IN}=V_{IN,NOM}, I_O=I_{O,MAX}, 25^{\circ}C)$ Case 1: T _{delay} = Time until V _O = 10% of V _{O,SET} from application of Vin with Remote On/Off set to ON,	T _{delay}		30		ms
Case 2: T_{delay} = Time until V_O = 10% of $V_{O,SET}$ from application of Remote On/Off from OFF to ON with V_{IN} already applied for at least one second.	T _{delay}	_	30		ms
T_{rise} = time for V_{0} to rise from 10% of $V_{0,\text{SET}}$ to 90% of $V_{0,\text{SET}}$	Td _{elay}	_	50	—	ms
Output Voltage Overshoot (Io=80% of Io, MAX, TA=25°C)				3	% V _{O, set}
Output Voltage Adjustment					
Output Voltage Remote-sense Range	Vsense	—	—	2	%V _{o,nom}
Output Voltage Set-point Adjustment Range (trim) Note: see Fig 6	Vtrim	15.0		35.2	Vdc

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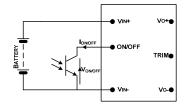
MQ9208ET540					
Output Overvoltage Protection	V _{O,limit}	37	_	42	Vdc
Over Temperature Protection (See Feature Descriptions, Fig 8)	Tref	_	120	_	°C
Input Under Voltage Lockout Turn-on Threshold Turn-off Threshold Hysteresis	Vin,uvlo	31	35 32 3	36	V _{dc} V _{dc} V _{dc}
Input Over voltage Lockout Turn-on Threshold Turn-off Threshold Hysteresis	Vin,uvlo	 81 	79.5 83 3	81 	V _{dc} V _{dc} V _{dc}

Test Configurations



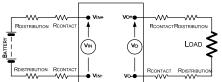
NOTE: Measure input reflected ripple current with a simulated source inductance (L_{TEST}) of 12µH. Capacitor CS offsets possible battery impedance. Measure current as shown above.

Fig 1. Input Reflected Ripple Current Test Setup



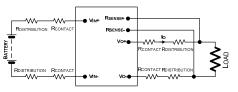
NOTE: An open collector switch is recommended for remote on/off control, controlling this switch to turn on or off the unit.

Fig 4. Remote On/Off



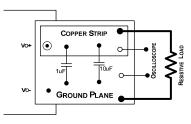
NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

Fig 2. Output Voltage and Efficiency Test Setup



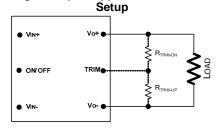
NOTE: Remote sense function is used to minimize the effects of distribution losses by regulating the voltage at the remote-sense connections.

Fig 5. Remote Sense



NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

Fig 3. Output Ripple and Noise Test



NOTE: Trimming function is allowed the output voltage set point to be adjusted from the default value in a allowed range.

Fig 6. Output Trim

 $\hat{\mathcal{U}}_{nion}$

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Input Filtering

The power module should be connected to a low ac-impedance source. Highly inductive source impedance can affect the stability of the power module. For the test configuration in **Fig1**, a 150 μ F low ESR electrolytic capacitor C_{IN}, mounted close to the power module helps ensure the stability of the unit. For more information, please consult the factory.

Conversation efficiency

To see Fig 2 for the test setup, the efficiency can be calculated as below:

Output Capacitance

The power module requires a minimum output capacitance of 220μ F Low ESR aluminum capacitor, C_{OUT} to ensure stable operation over the full range of load and line conditions, see **Fig 3**. If the ambient temperature is under -20°C, it is required to use at least 3pcs of minimum capacitors in parallel. In general, the process of determining the acceptable values of output capacitance and ESR is complex and load-dependent.

Safety Considerations

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e. UL60950-1, CSA C22.2 No.60950-1, and VDE0805-1(IEC60950-1).

For end products connected to -48V dc, or -60Vdc nominal DC MAINS (i.e. central office dc battery plant), no further fault testing is required.

For all input voltages, other than DC MAINS, where the input voltage is less than 60V dc, if the input meets all of the requirements for SELV, then:

- The output may be considered SELV. Output voltages will remain within SELV limits even with internally-generated non-SELV voltages. Single component failure and fault tests were performed in the power converters.
- One pole of the input and one pole of the output are to be grounded, or both circuits are to be kept floating, to maintain the output voltage to ground voltage within ELV or SELV limits. However, SELV will not be maintained if VI(+) and VO(+) are grounded simultaneously.

For all input sources, other than DC MAINS, where the input voltage is between 60 and 75V dc (Classified as TNV-2 in Europe), the following must be meet, if the converter's output is to be evaluated for SELV:

- The input source is to be provided with reinforced insulation from any other hazardous voltages, including the ac mains.
- One VIN pin and one VOUT pin are to be grounded, or both the input and output pins are to be kept floating.
- Another SELV reliability test is conducted on the whole system, as required by the safety agencies, on the combination of supply source and the subject module to verify that under a single fault, hazardous voltages do not appear at the module's output.
- □ The input pins of the module are not operator accessible.

All flammable materials used in the manufacturing of these modules are rated 94V-0, or tested to the UL60950 A.2 for reduced thickness.

The input to these units is to be provided with a maximum 10A fast-acting fuse in the ungrounded lead.



MQ9208ET540

Remote On/Off

Two remote on/off options are available. Positive logic (device code suffix "P") turns the module on during a logic high voltage on the ON/OFF pin, and off during a logic low. Negative logic remote On/Off, device code suffix "N", turns the module off during a logic high and on during a logic low.

To turn the power module on and off, the user must supply a switch (open collector or equivalent) to control the voltage (V_{ON/OFF}) between the ON/OFF terminal and the V_{IN(-)} terminal, see **Fig 4.** Logic low is $0V \le V_{ON/OFF} \le 0.6V$. The maximum I_{ON/OFF} during a logic low is 0.15mA; the switch should maintain a logic low level whilst sinking this current. During a logic high, the typical maximum V_{ON/OFF} generated by the module is 5V, and the maximum allowable leakage current at V_{ON/OFF} = 5V is 1µA.

If not using the remote on/off feature:

- $\hfill\square$ For positive logic, leave the ON/OFF pin open.
- $\hfill\square$ For negative logic, short the ON/OFF pin to $V_{IN(\text{-}).}$

Remote Sense

Remote sense minimizes the effects of distribution losses by regulating the voltage at the remote-sense connections, see **Fig 5.** for the detail configuration. The voltage between the remote-sense pins and the output terminals must not exceed the output voltage sense range given in the Feature Specifications table:

$[V_{O(+)} - V_{O(-)}] - [SENSE(+) - SENSE(-)] \le 0.5V$

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module.

Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power (Maximum rated power = Vo,SET X IO,MAX).

Output Trim

MQ9208ET540 output can be trimmed up or down by connecting one resistor to output negative or positive end as **Fig 6**. Connecting an external resistor (R_{DN}) between the TRIM pin and the V₀(+) (or Sense (+)) pin decreases the output voltage set point. To maintain set point accuracy, the trim resistor tolerance should be ±1.0% or better. The following equation determines the required external resistor value:

$$R_{\rm DN} = (\frac{5.11*V_{O,SET}}{V_{O,SET} - V_{O,DN}} - 10.22) \rm K\Omega$$

 $V_{O,SET}$ is the output default set-point voltage of the module, $V_{O,DN}$ is the desired trim-down output voltage.

Connecting an external resistor (R_{UP}) between the TRIM pin and the V₀(-) (or Sense(-)) pin increases the output voltage set point. The following equation determines the required external resistor value:

$$R_{\rm UP} = \left[\frac{5.11 * V_{0,SET}}{V_{0,UP} - V_{0,SET}} * \left(\frac{V_{0,UP}}{1.225} - 1\right) - 10.22\right] \mathrm{K}\Omega$$

V_{O,SET} is the output set-point voltage of the module, V_{O.UP} is the desired trim-up output voltage.

Input Under-Voltage Lockout

At input voltages below the input under-voltage lockout limit, the module operation is disabled. The module will only begin to operate once the input voltage is raised above the under-voltage lockout turn-on threshold, $V_{UV/ON}$. Once operating, the module will continue to operate until the input voltage is taken below the under-voltage turn-off threshold, $V_{UV/OFF}$.



Over-temperature Protection

To provide protection under certain fault conditions, the unit is equipped with a thermal shutdown circuit. The unit will shutdown if the thermal reference points, Tref, exceed135°C(typical)respectively, but the thermal shutdown is not intended as a guarantee that the unit will survive temperatures beyond its rating. The module will automatically restart upon cool-down to a safe temperature.

Output Overvoltage Protection

The output over voltage protection scheme of the modules has an independent over voltage loop to prevent single point of failure. This protection feature latches in the event of over voltage across the output. Cycling the on/off pin or input voltage resets the latching protection feature. If the auto-restart option is ordered, the module will automatically restart upon an internally programmed time elapsing.

Over current Protection

To provide protection in a fault output overload condition, the module is equipped with internal current limiting protection circuitry, and can endure continuous over current by providing constant current output, for up to 4 seconds, as long as the output voltage is greater than VTRIM,MIN. If the load resistance is too low to support $V_{TRIM,MIN}$ in an over current condition or a short circuit load condition exists, the module will shut down immediately.

An auto-restart option is standard. Following shutdown, the module will restart after a period of 3 seconds if the shutdown happens due to over-current protection being triggered or the module will restart after a period of 2.5 seconds when the shutdown happens due to output over-voltage protection getting enabled. A latching shutdown option (4) is also available in a case where an auto recovery is required. If over current greater than 4A persists for few milli-seconds, the module will shut down and auto restart until the fault condition is corrected. If the output overload condition still exists when the module restarts, it will shut down again. This operation will continue indefinitely, until the over current condition is corrected. If the auto-restart option, it will remain in the hiccup mode as long as the Over current condition exists; it operates normally, once the output current is brought back into its specified range. The average output current during hiccup is 10% lo, max.

Thermal Considerations

The power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation. Considerations include ambient temperature, airflow, module power dissipation, and the need for increased reliability. A reduction in the operating temperature of the module will result in an increase in reliability. The thermal data presented here is based on physical measurements taken in a wind tunnel, using automated thermocouple instrumentation to monitor key component temperatures: FETs, diodes, control ICs, magnetic cores, ceramic capacitors, opto-isolators, and module pwb conductors, while controlling the ambient airflow rate and temperature. For a given airflow and ambient temperature, the module output power is increased, until one (or more) of the components reaches its maximum derated operating temperature, as defined in IPC-9592. This procedure is then repeated for a different airflow or ambient temperature until a family of module output derating curves is obtained. The output power of the module should not exceed the rated power for the module as listed in the ordering Information table. Although the maximum T_{REF} temperature of the power modules is discussed above, you can limit this temperature to a lower value for extremely high reliability.

The thermal reference points, T_{REF} , used in the specifications for this module is shown in Fig8. For reliable operation, the temperature should not exceed 115°C.



Fig 8 Case Temperature (TREF) Measurement Location (Top view).

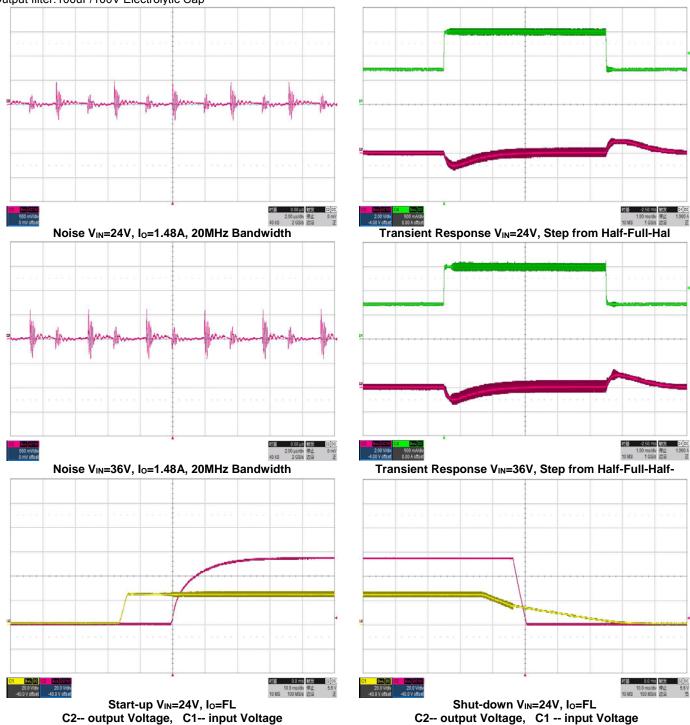
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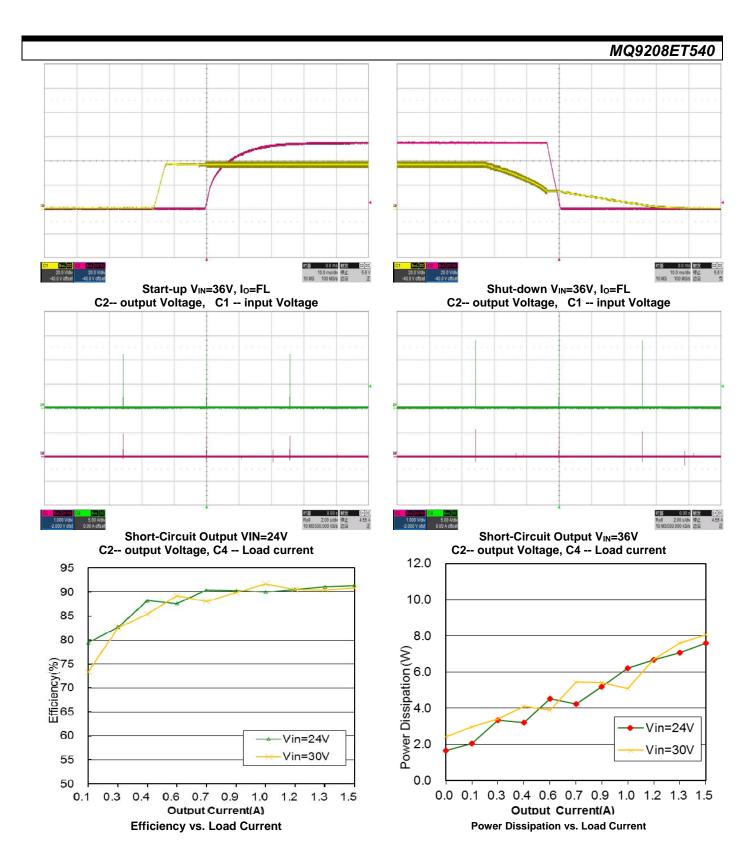
Typical Characteristics- output adjusted to 54V

General conditions: Input filter: 100uF/100V Electrolytic cap

Output filter:100uF/100V Electrolytic Cap

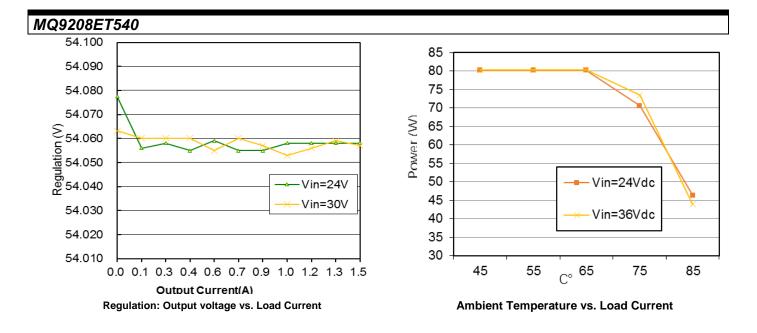


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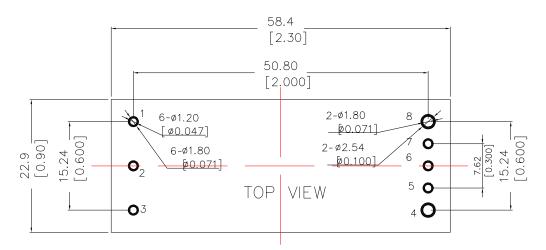


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Recommended Hole Pattern

Unit: millimeters (inches)

Tolerances: x.x ±0.5 mm (0.02in), x.xx ±0.25 mm (0.010in), unless otherwise noted

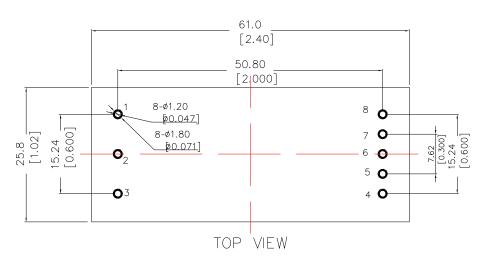


Component side footprint

Recommended Hole Pattern for "HS"

Unit: millimeters (inches)

Tolerances: x.x ±0.5 mm (0.02in), x.xx ±0.25 mm (0.010in), unless otherwise noted



Component side footprint



Application Notes



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